

Figure 1

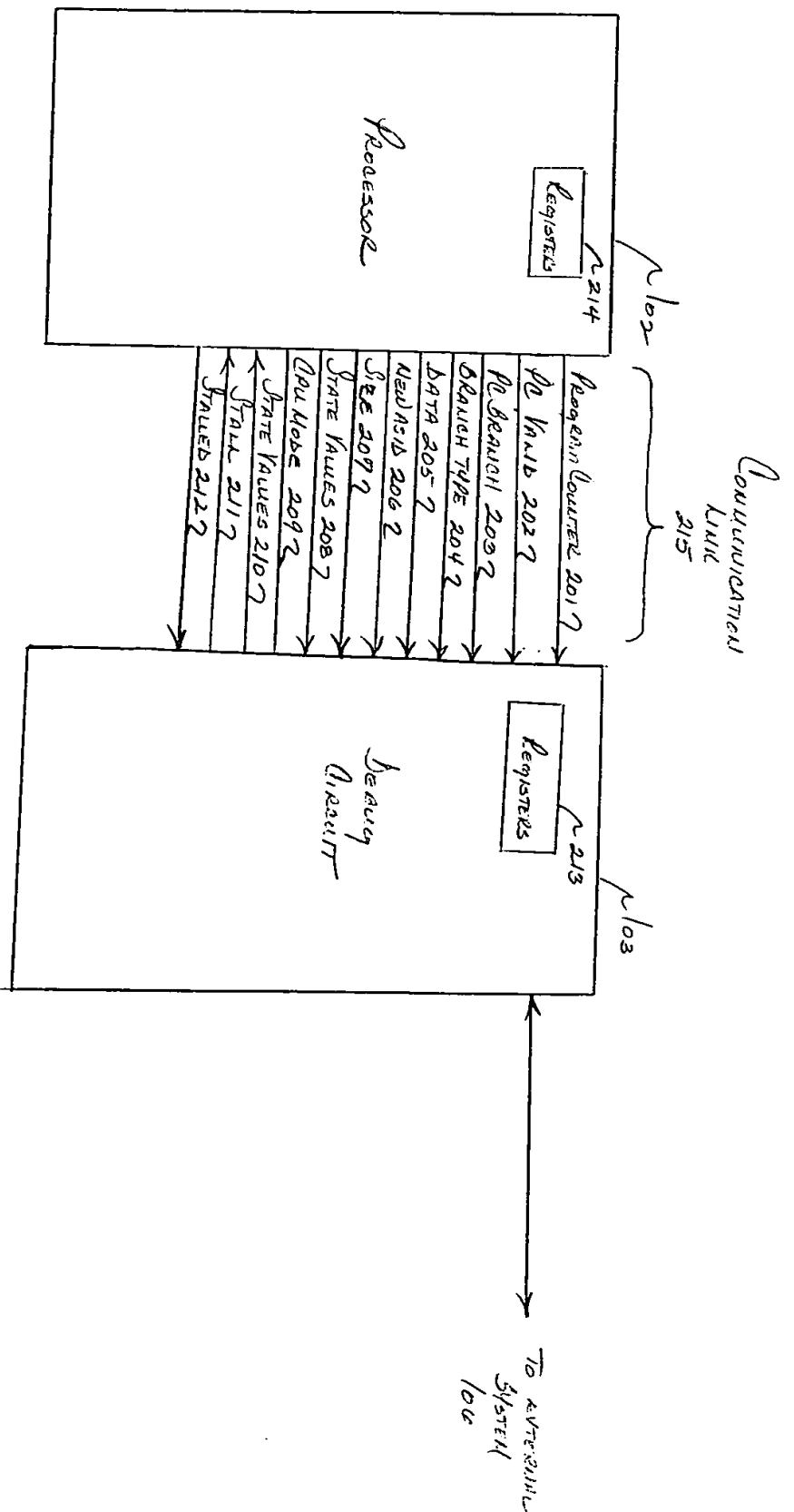


Figure 2

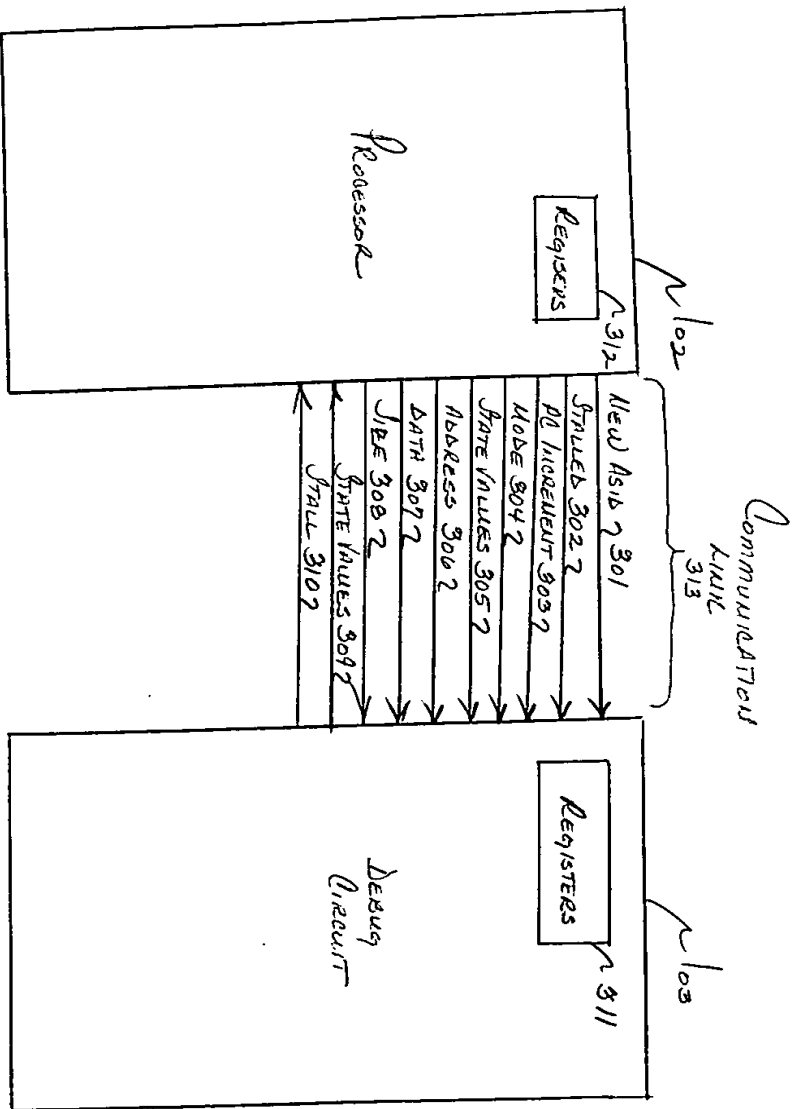
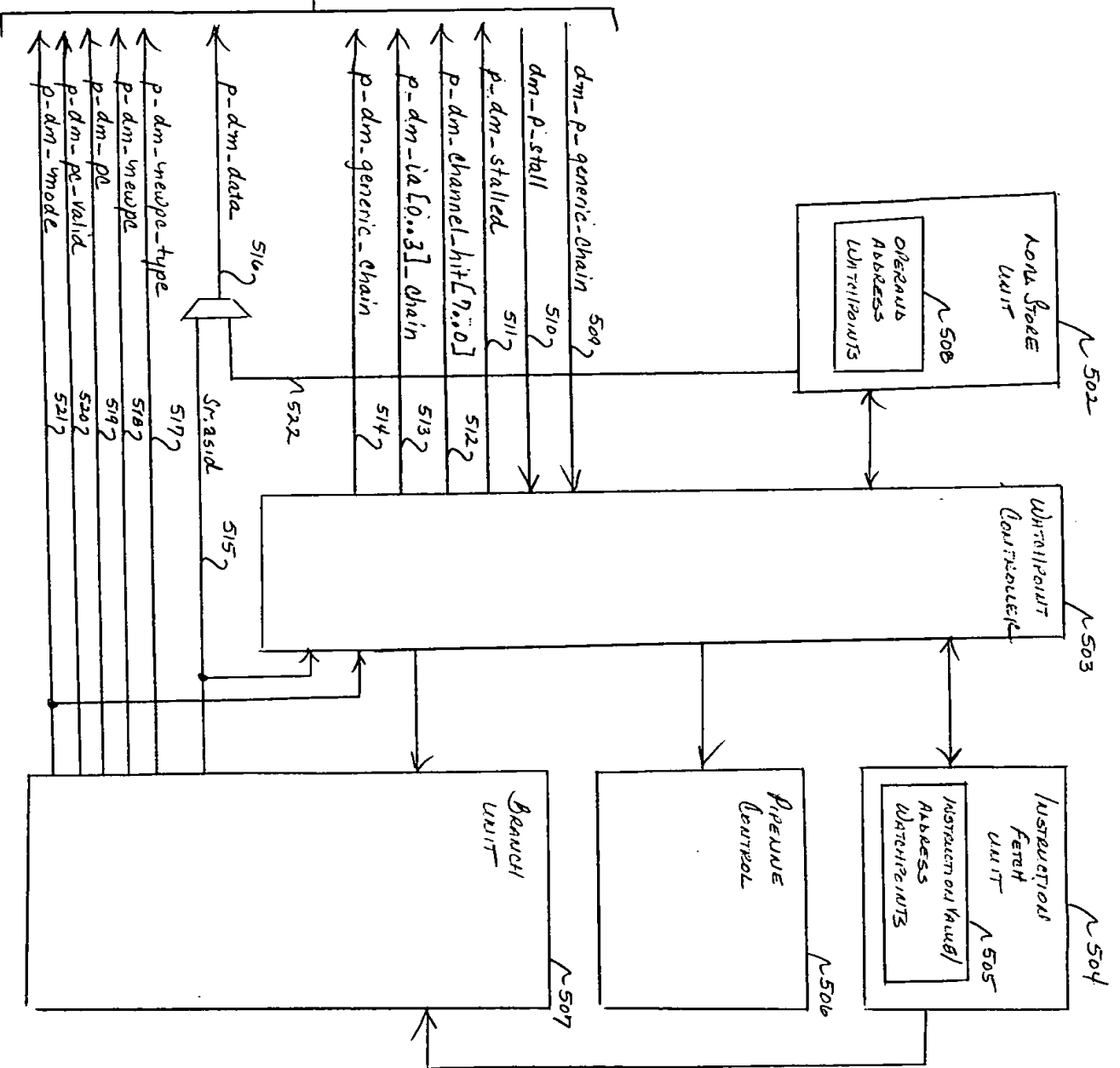


Figure 3



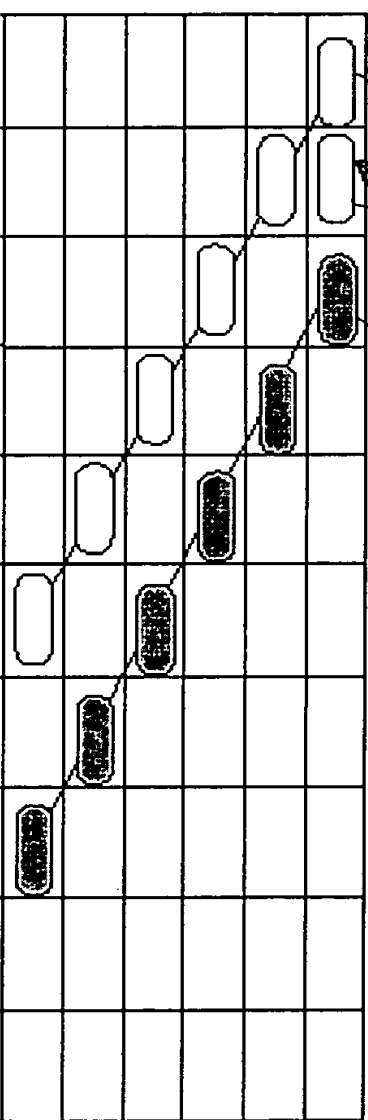
Processor  
501



522  
 Direct  
 To Debby

SECRET

F  
D  
E1  
E2  
E3  
W



BLINK(I) I' discarded  
ADD (T)  
601 602 603

P<sub>dm</sub>pc  
P<sub>dm</sub>pc\_valid  
P<sub>dm</sub>newpc  
P<sub>dm</sub>newpc\_type

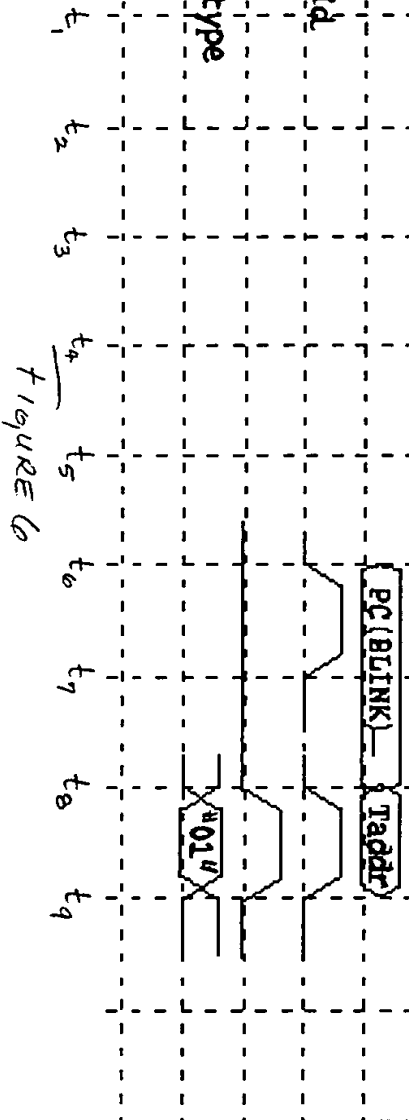


figure 6